

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

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APPLICATION NO. 09/863650	CONT/PRIOR D	CLASS 708	SUBCLASS 54	ART UNIT 2121	EXAMINER 790
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APPLICANTS

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TITLE

Apparatus and method for implementing efficient arithmetic circuits
in programmable logic devices

PTO-2040
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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